

Fig. 1



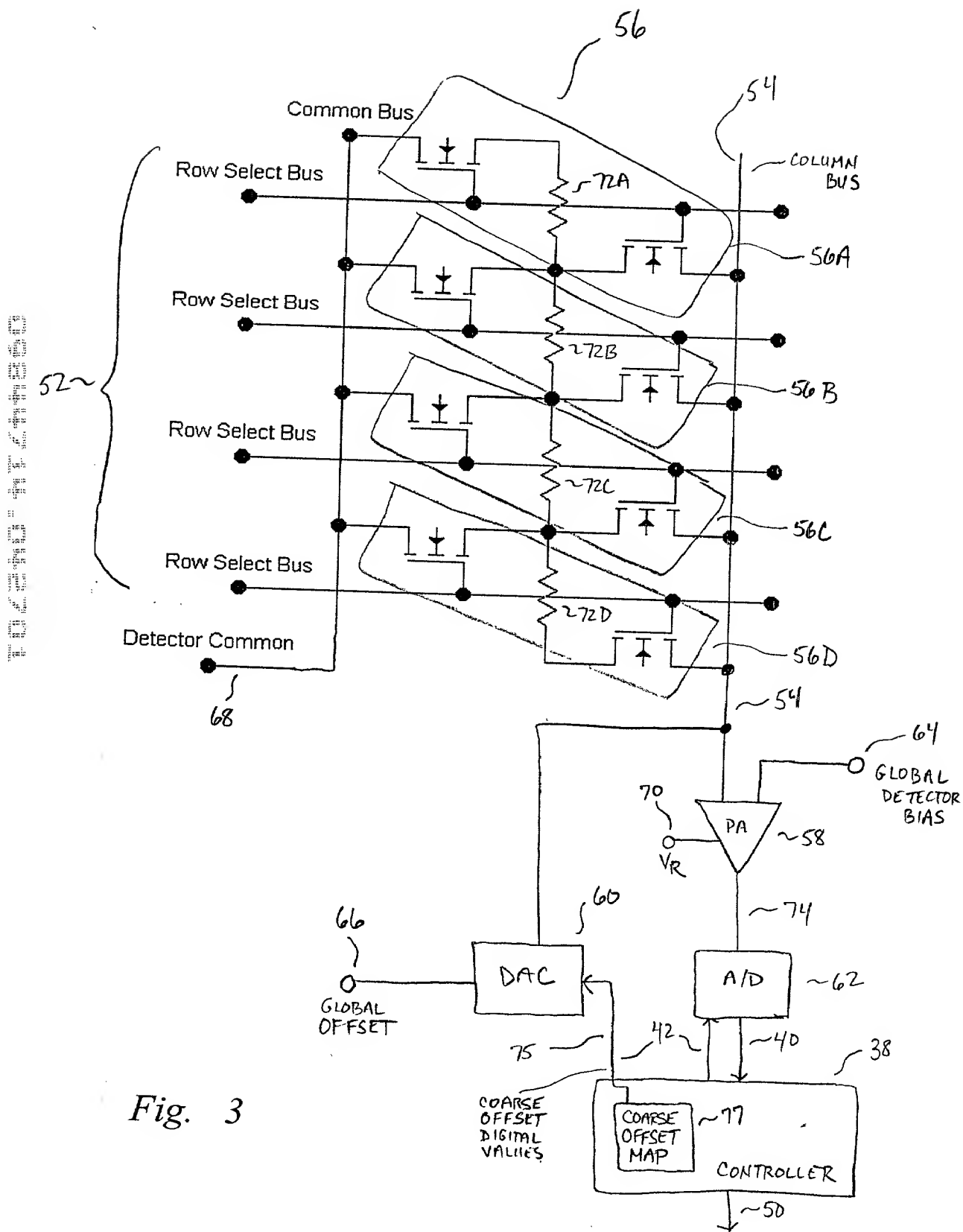


Fig. 3

75

(COARSE OFFSET  
DIGITAL VALUES  
FROM CONTROLLER 38)

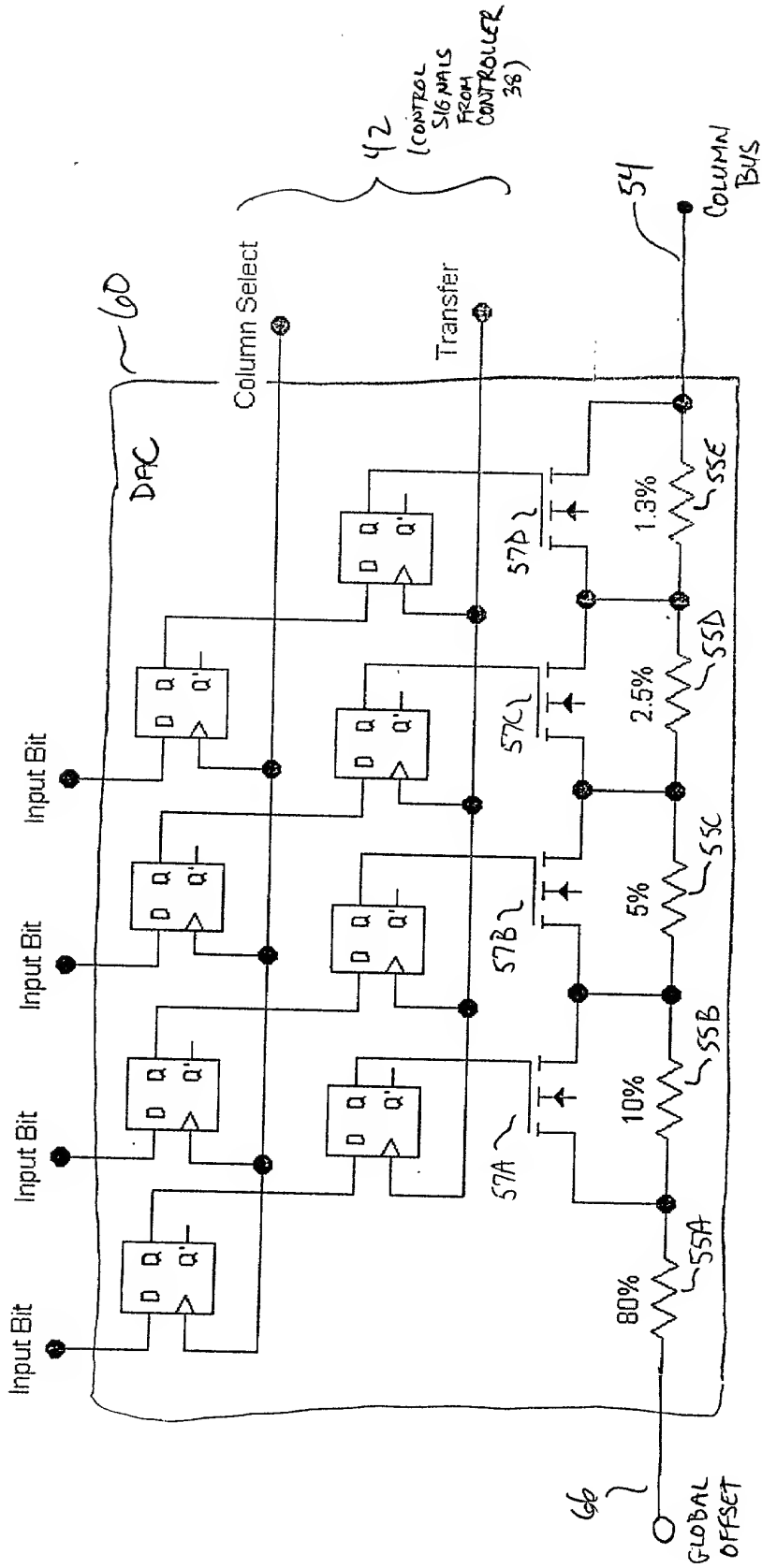


Fig. 4

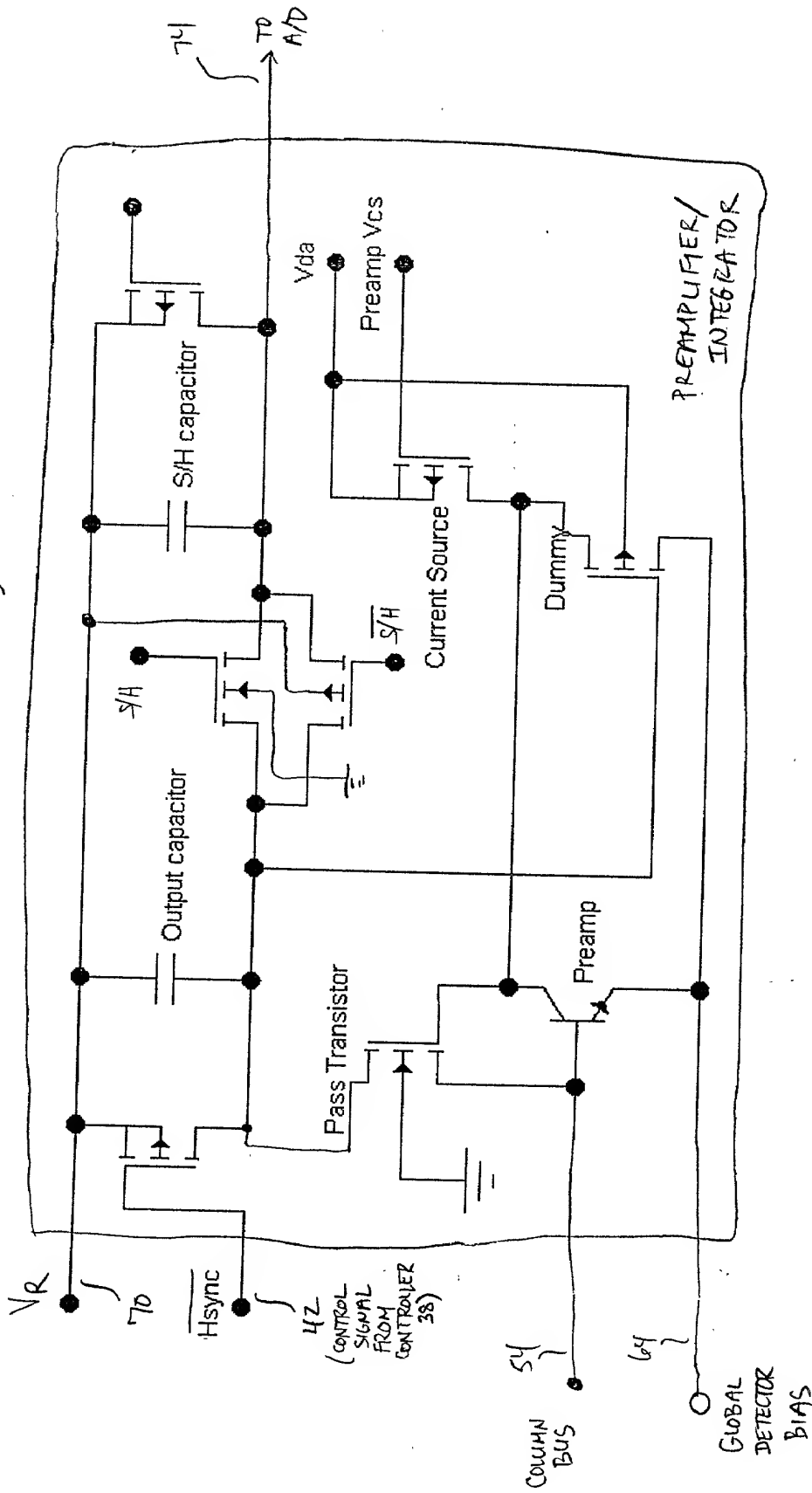


Fig. 5

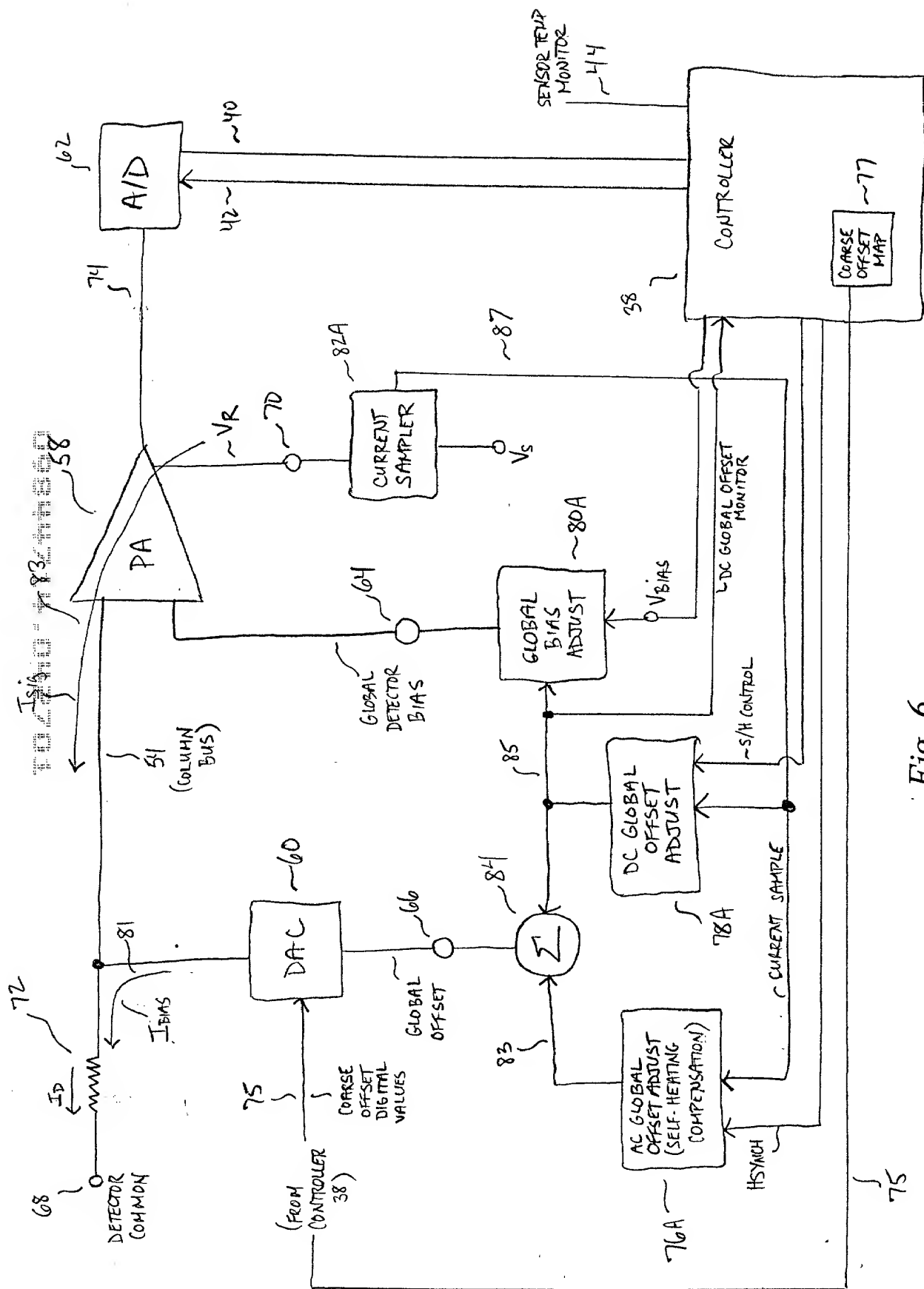


Fig. 6

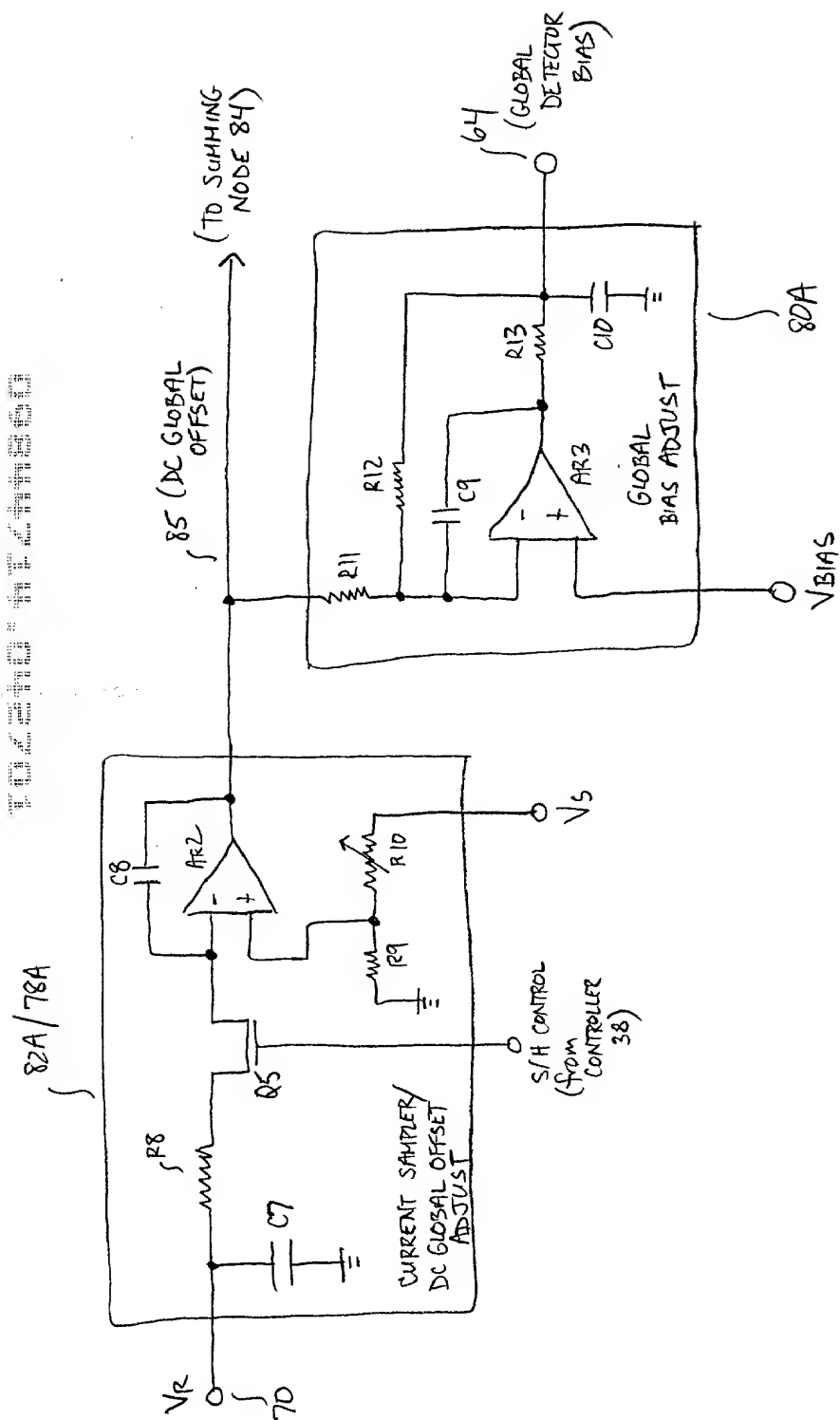


Fig. 7

82A/76A

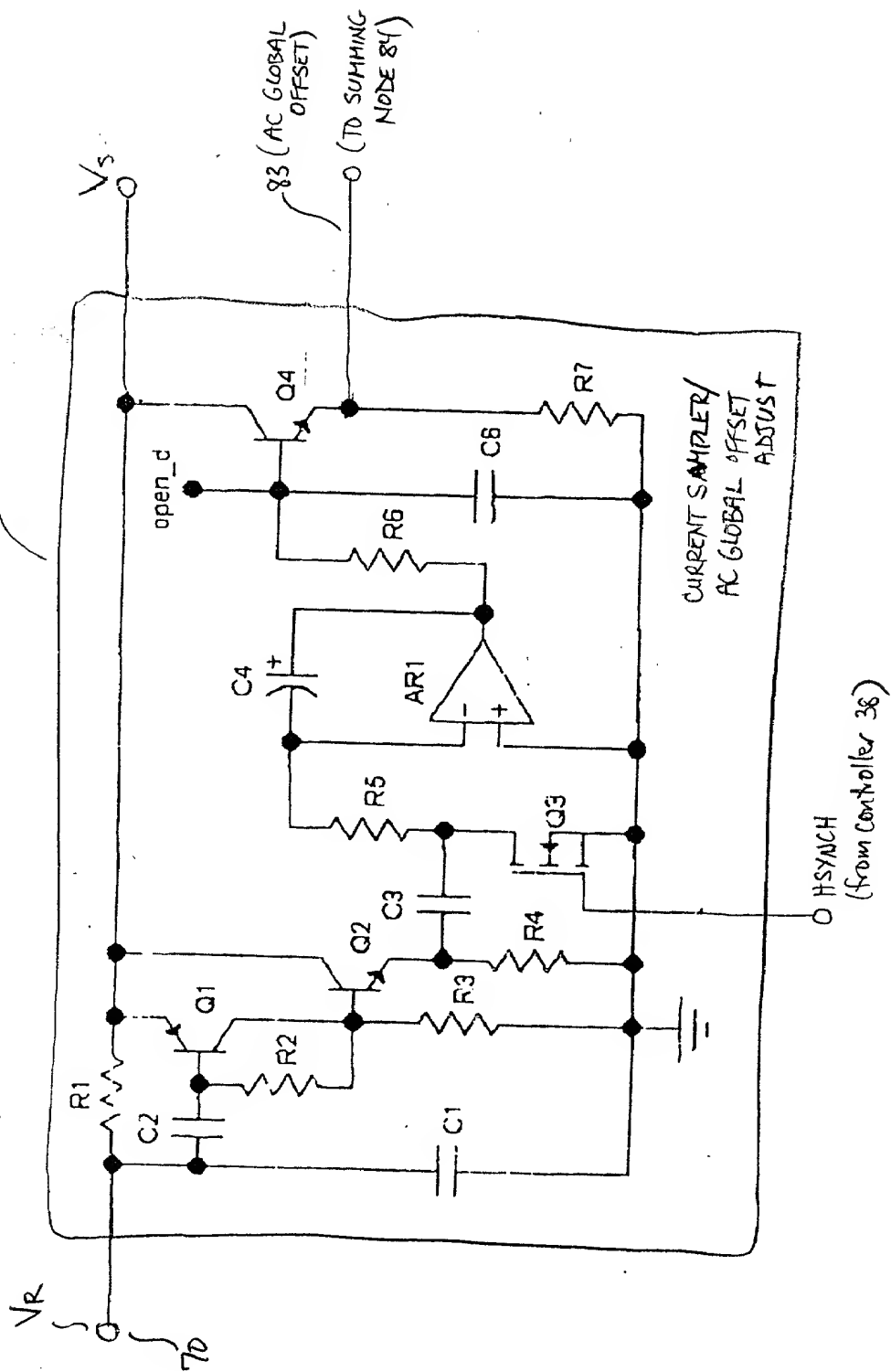


Fig. 8



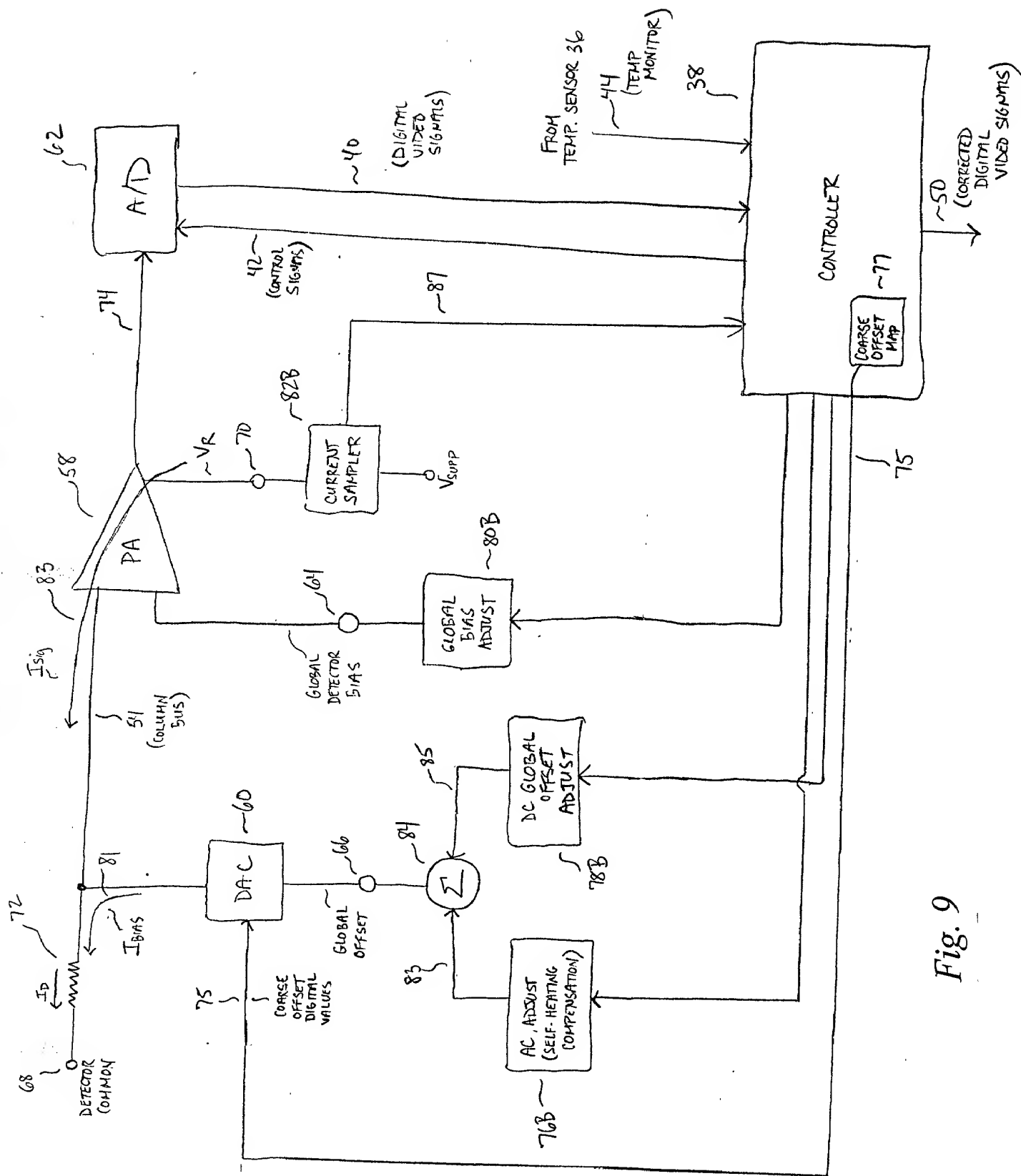


Fig. 9

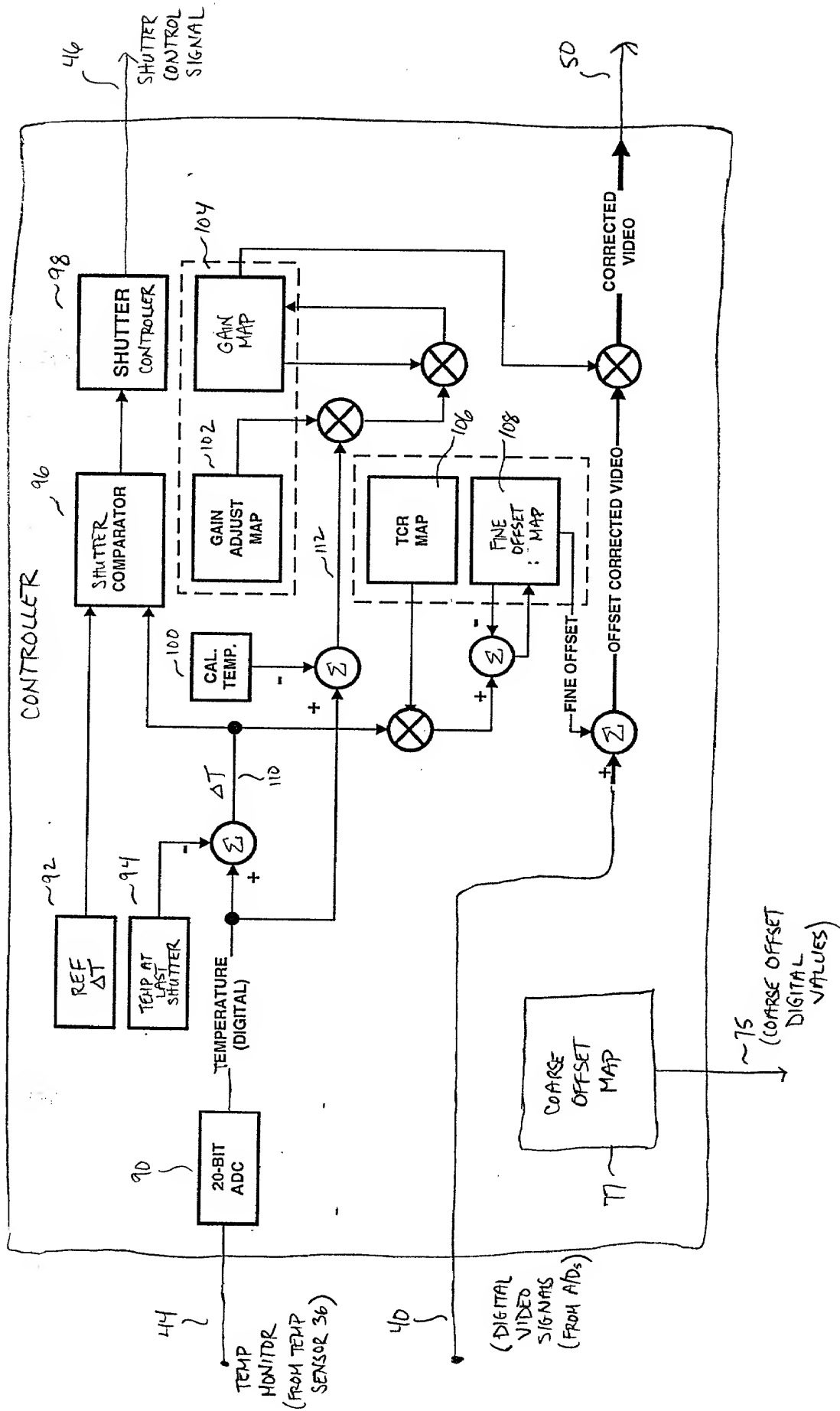


Fig. 10

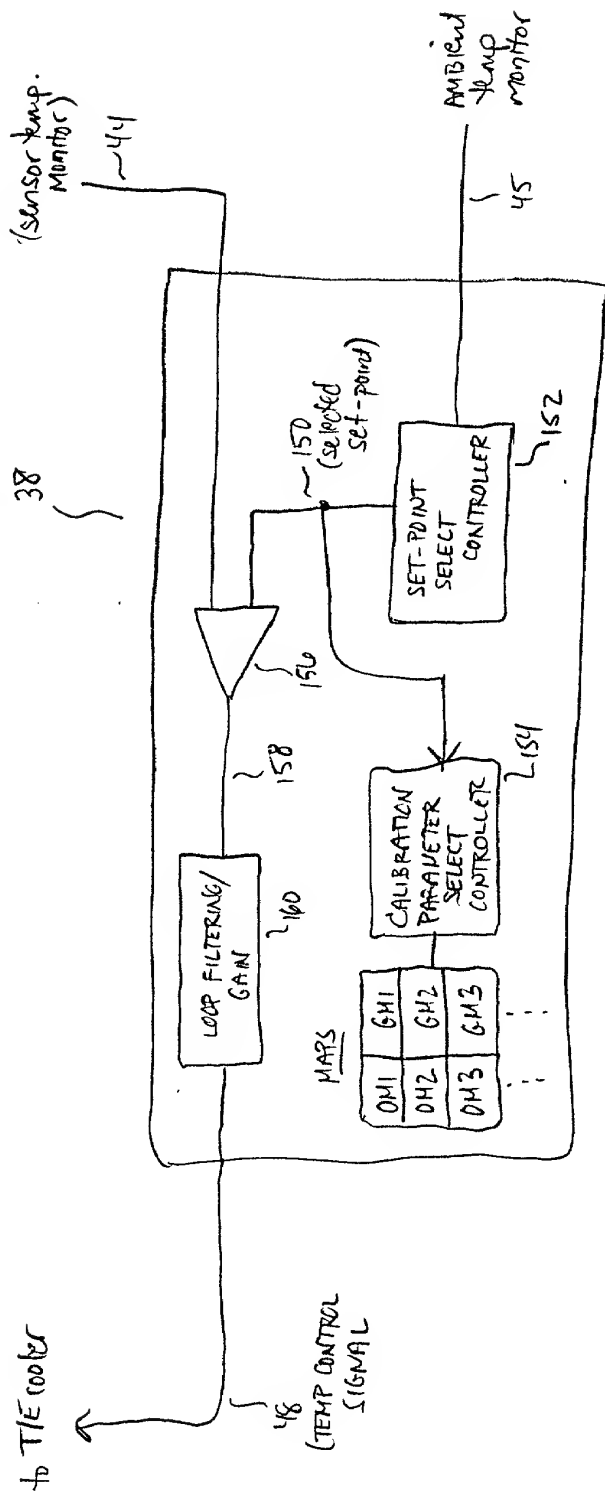


Fig. 11